

# CALL FOR PAPERS

**SUBMISSION DEADLINE: APRIL 11TH, 2018**

**OCTOBER 24-25, 2018 | HOLIDAY INN MUNICH, CITY CENTRE**

The Design and Verification Conference & Exhibition Europe (DVCon Europe) is the premier conference for system architects, concept engineers, software developers, design and verification engineers, and IP integrators to share the latest methodologies and technologies in the practical use of EDA and IP languages and standards used in electronic design.

The focus of this highly technical conference is on the industrial application of specialized design and verification languages such as SystemC, SystemVerilog, VHDL, UVM or e; assertions in SVA or PSL; the use of AMS languages; design automation using IP-XACT; and the use of general purpose languages C and C++.

This call for papers solicits presentations that are highly technical and reflect real life experiences in using EDA languages, standards, methodologies and tools. Industry applications of interest include (but are not limited to) automotive, mobile communication, aerospace, healthcare, chip-cards, consumer and power electronics. Submissions are encouraged in (but not restricted to) the four topic areas listed below.

## TOPIC AREA 1: SYSTEM-LEVEL DESIGN

- Virtual and hardware-assisted prototyping
- Hardware/software/embedded co-design
- System-on-chip and network-on-chip design
- High-level synthesis from ESL languages

## TOPIC AREA 2: VERIFICATION & VALIDATION

- Verification process, reuse and resource management
- Methods bridging between verification and validation
- Hardware/software co-verification
- Advanced methodologies, testbenches, and flows (e.g., UVM, HDLs, HVLs)
- Formal and semi-formal V&V techniques

## TOPIC AREA 3: IP REUSE & DESIGN AUTOMATION

- Interoperability of models and/or tools
- IP tagging, protection or security
- SoC and IP integration methods, flows, and tools
- Configuration management of IPs including different abstraction level)
- Flow and tool automation (e.g., IP-XACT)

## TOPIC AREA 4: FUNCTIONAL SAFETY AND SECURITY

- Methods and flows for functional safety standard compliance (e.g., ISO 26262, DO-254)
- Safety and security in verification and validation
- Requirements-driven design and verification including traceability
- New methods and tools supporting functional safety and security

## TOPIC AREA 5: MIXED-SIGNAL AND LOW-POWER DESIGN AND VERIFICATION

- AMS modeling for concept and system-level design
- Application of mixed-signal extensions in verification (e.g., UVM-MS)
- Real-number modeling approaches
- Self-checking testbenches in analog verification

## CONFERENCE SCHEDULE

**October 24, 2018:** Tutorials & Exhibition

**October 25, 2018:** Technical Paper Sessions, Poster Session, & Exhibition

## PAPER SUBMISSION PROCESS

### Note: New Submission Guidelines for DVCon Europe 2018

An initial submission is required before the full paper. The submission should be between a minimum of 600 words and a maximum of 1200 words (approximately 2 pages, not including diagrams, figures or tables). In general, please provide enough details so that the Technical Program Committee can evaluate the potential quality and interest of your proposed presentation at DVCon Europe.

The initial submission should include:

- **Title:** The paper title.
- **Contact information:** Name, affiliation, phone number and email address for all authors.
- **Short Abstract:** Outline that clearly states the context and motivation of your contribution, approx. 100 words.
- **Related Work:** Identify other work on which this submission will be built, and the novelty of the paper.
- **Application:** Clearly describe the technical contribution, reflect real life experiences, and its industrial application.
- **(Preliminary) results:** Summarize the results. State how these differ from previous work or state-of-the-art on the same subject.
- **Conclusions:** Summarize major conclusions and findings presented in the paper.

Accepted authors will be invited and agree to do the following:

- Submit the final version of the paper (max. 8 pages) after incorporating feedback from the TPC (July 31, 2018)
- Register for the conference
- Submit a copyright form
- All accepted authors agree to present an oral or poster presentation at the conference (**October 24-25, 2018**).

**Please note:** Consistent with the requirements for other DVCon Europe presentations, your presentation may contain your company logo only on the title slide, and should use the provided templates.

Please do your initial submission via the Navigation Center by **April 11, 2018**. The Navigation Center will open on March 7th and a link will be posted on the conference website.

## QUESTIONS?

Feel free to contact us for questions on the submission process at [support@mpassociates.com](mailto:support@mpassociates.com) or Jackie McIntosh at [jackie@mpassociates.com](mailto:jackie@mpassociates.com).

DVCon Europe honors the Best Paper/Presentation and Best Poster submissions. The awards will be selected by the attendees at DVCon Europe based on the quality of both the paper and the presentation.

More information on DVCon Europe can be found on [www.dvcon-europe.org](http://www.dvcon-europe.org)

### Program Chairs:

Alexander.Rath, *Infineon*  
Matthias Bauer, *Infineon*  
Mark Burton, *GreenSocs*

## IMPORTANT DEADLINES

- **April 11, 2018:** Deadline for initial submission
- **May 30, 2018:** Accept/reject notification sent to all authors
- **June 20, 2018:** Full paper due for review by TPC
- **September 5, 2018:** Final paper due
- **October 24-25, 2018:** Conference.