



CALL FOR PAPERS

SUBMISSION DEADLINE: APRIL 9th, 2019

OCTOBER 29-30, 2019 | HOLIDAY INN MUNICH, CITY CENTRE

The Design and Verification Conference & Exhibition Europe (DVCon Europe) is the premier conference for the engineering community active in system, software, design, verification, validation or integration to share the latest methodologies and technologies on the practical use of EDA and IP languages and standards for integrated and embedded systems and products.

The focus of this highly technical conference is on the industrial application of specialized design, verification and validation methodologies, design and verification languages such as C, SystemC, SystemVerilog, UVM, formal and assertion-based verification; the use of AMS languages; design automation using IP-XACT and the use of other general purpose languages.

Industry applications of interest are (but not limited to) automotive, mobile communication, aerospace, healthcare, chip-cards, consumer and power electronics. DVCon Europe solicits submissions related to advanced design and verification on special interest areas such as Digital Twin, Machine Learning, Internet-of-things and Functional safety and security.

This call for papers solicits presentations that are highly technical and reflect real life experiences in using EDA languages, standards, methodologies and tools. Submissions are encouraged in (but not restricted to) the topic areas listed below.

TOPIC AREA 1: SYSTEM-LEVEL DESIGN

- Virtual and hardware-assisted prototyping
- Hardware/software/embedded co-design
- System-on-chip and network-on-chip design
- High-level synthesis from ESL languages

TOPIC AREA 2: VERIFICATION & VALIDATION

- Verification process, reuse and resource management
- Methods bridging between verification and validation
- Hardware/software co-verification
- Advanced methodologies, testbenches, and flows (e.g., UVM, HDLs, HVLs)
- Formal and semi-formal V&V techniques

TOPIC AREA 3: IP REUSE & DESIGN AUTOMATION

- Interoperability of models and/or tools
- IP tagging, protection or security
- SoC and IP integration methods, flows, and tools
- Configuration management of IPs including different abstraction level)
- Flow and tool automation (e.g., IP-XACT)

TOPIC AREA 4: FUNCTIONAL SAFETY AND SECURITY

- Methods and flows for functional safety standard compliance (e.g., ISO 26262, DO-254)
- Safety and security in verification and validation
- Requirements-driven design and verification including traceability
- New methods and tools supporting functional safety and security

TOPIC AREA 5: MIXED-SIGNAL AND LOW-POWER DESIGN AND VERIFICATION

- AMS modeling for concept and system-level design
- Application of mixed-signal extensions in verification (e.g., UVM-MS)
- Real-number modeling approaches
- Self-checking testbenches in analog verification

NEW TOPIC AREA 6 : MODEL-BASED AND MODEL SUPPORTED SOFTWARE DESIGN

- Software for verification
- Model based software design
- Low level software design and verification
- Model based tools and techniques for application level software.

CONFERENCE SCHEDULE

October 29, 2019: Tutorials & Exhibition

October 30, 2019: Technical Paper Sessions, Poster Session, & Exhibition

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PAPER SUBMISSION PROCESS

Note: New Submission Guidelines for DVCon Europe 2019

An initial submission is required before the full paper. The submission should be between **a minimum of 600 words and a maximum of 1200 words (approximately 2 pages, not including diagrams, figures or tables)**.

In general, please provide enough details so that the Technical Program Committee can evaluate the potential quality and interest of your proposed presentation at DVCon Europe.

The initial submission should include:

- **Title:** The paper title.
- **Contact information:** Name, affiliation, phone number and email address for all authors.
- **Short Abstract:** Outline that clearly states the context and motivation of your contribution, approx. 100 words.
- **PDF Upload:** 2 pages minimum without diagrams/pictures/etc required. If you would like to include diagrams/pictures/etc. the maximum amount of pages you can submit is 8. In your PDF, please include:
 - **Related Work:** Identify other work on which this submission will be built, and the novelty of the paper.
 - **Application:** Clearly describe the technical contribution, reflect real life experiences, and its industrial application.
 - **(Preliminary) results:** Summarize the results. State how these differ from previous work or state-of-the-art on the same subject.
 - **Conclusions:** Summarize major conclusions and findings presented in the paper.

Accepted authors will be invited and agree to do the following:

- Submit the final version of the paper (max. 8 pages) after incorporating feedback from the TPC (July 31, 2019)
- Register for the conference
- Submit a copyright form
- All accepted authors agree to present an oral or poster presentation at the conference (**October 29-30, 2019**).

Please note: Consistent with the requirements for other DVCon Europe presentations, your presentation may contain your company logo only on the title slide, and should use the provided templates.

Please do your initial submission via the Navigation Center by **April 9, 2019**.

IMPORTANT DEADLINES

- **April 9, 2019:** Deadline for initial submission
- **May 28, 2019:** Accept/reject notification sent to all authors
- **June 18, 2019:** Full paper due for review by TPC
- **September 16, 2019:** Final paper due
- **October 29-30, 2019:** Conference.

QUESTIONS?

Feel free to contact us for questions on the submission process at support@mpassociates.com or Jackie Sweet at jackie@mpassociates.com.

DVCon Europe honors the Best Paper/Presentation and Best Poster submissions. The awards will be selected by the attendees at DVCon Europe based on the quality of both the paper and the presentation.

More information on DVCon Europe can be found on dvcon-europe.org

DVCon Europe Technical Program Chair:

Alexander Rath, Infineon
alexander.rath@infineon.com