



**October 16-17, 2017 Holiday Inn Munich, City Centre**

## CALL FOR PAPERS

The Design and Verification Conference & Exhibition Europe (DVCon Europe) is the premier conference for system architects, concept engineers, software developers, design and verification engineers, and IP integrators to share the latest methodologies and technologies on the practical use of EDA and IP languages and standards used in electronic design.

The focus of this highly technical conference is on the industrial application of specialized design and verification languages such as SystemC, SystemVerilog, VHDL, UVM or e; assertions in SVA or PSL; the use of AMS languages; design automation using IP-XACT; and the use of general purpose languages C and C++.

This call for papers solicits presentations that are highly technical and reflect real life experiences in using EDA languages, standards, methodologies and tools. Industry applications of interest include (but are not limited to) automotive, mobile communication, aerospace, healthcare, chip-cards, consumer and power electronics. Submissions are encouraged in (but not restricted to) the four topic areas listed below. Low power techniques and design for functional safety (e.g., ISO 26262, DO-254) are pervasive and can be addressed in any of these topics areas.

### Topic area 1: System-level design

- Requirements-driven design including traceability
- Virtual and hardware-assisted prototyping
- Architecture exploration
- Hardware/software/embedded co-design
- System-on-chip and network-on-chip design
- System development methodologies and flows
- High-level synthesis from ESL languages
- Safety and security in system-level design

### Topic area 2: Verification & Validation

- Requirements-driven verification including traceability
- Verification process, reuse and resource management
- Methods bridging between verification and validation
- Hardware/software co-verification

- Advanced methodologies, testbenches, and flows (e.g., UVM, HDLs, HVLs, testbench automation)
- Testbench qualification
- Formal and semi-formal techniques
- Safety and security in verification and validation

### Topic area 3: IP reuse and design automation

- Interoperability of models and/or tools
- IP tagging, protection or security
- SoC and IP integration methods, flows, and tools
- Configuration management of IPs including different abstraction levels
- Flow and tool automation (e.g., IP-XACT)

### Topic area 4: Mixed-signal design and verification

- AMS concept and system design
- Application of mixed-signal extensions (e.g., UVM)
- Real-number modeling approaches
- Mixed-signal design and verification techniques (applied on proper abstraction level)
- Self-checking in analog verification

## PAPER SUBMISSION PROCESS

Note that a **draft version** of the paper is sufficient at this stage. A draft paper should contain at least:

- **Title:** The paper title.
- **Contact information:** Name, affiliation, phone number and email address for all authors.
- **Abstract:** Outline that clearly states the context and motivation of your contribution, approx. 100 words.
- **Application:** Clearly describe the technical contribution, reflects real life experiences, and its industrial application.
- **(Preliminary) results:** Summarize the results, including facts and figures. State how these differ from previous work or state-of-the-art on the same subject.
- **Conclusions:** Major conclusions and findings presented in the paper.
- **Relevance of the paper:** Describe the significance and/or benefits of the proposed paper in a short list.
- **Draft paper:** number of pages max. 8, draft paper shall be close to final paper
- **Full paper:** number of pages max. 8

In general, please provide enough details so that the Technical Program Committee can evaluate the potential quality and interest of your proposed presentation at DVCon Europe.

Draft and full paper requirements and templates can be found in our Resource Center.

## IMPORTANT DEADLINES

- **April 4th:** Draft paper submission deadline (closed)
- **June 12th:** Accept/reject Notification sent to all authors
- Accepted authors will be invited and agree to do the following by **August 10th:**
  - Submit the final version of the paper (max. 8 pages)
  - Register for the conference
  - Submit a copyright form
  - All accepted authors agree to present an oral or poster presentation at the conference on **October 16-17, 2017.**

**Please note:** Consistent with the requirements for other DVCon Europe presentations, your presentation may contain your company logo only on the title slide.

## CONFERENCE SCHEDULE

October 16, 2016 — Tutorials and exhibition

October 17, 2016 — Technical paper sessions, poster session, exhibition

Questions?

Feel free to contact us for questions on the submission process at [support@mpassociates.com](mailto:support@mpassociates.com) or Jackie McIntosh [Jackie@mpassociates.com](mailto:Jackie@mpassociates.com)

DVCon Europe honors the Best Paper/Presentation and Best Lightening Talk submissions. The awards will be selected by the attendees at DVCon Europe based on the quality of both the paper and the presentation.



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## CALL FOR TUTORIALS

The Design and Verification Conference & Exhibition Europe (DVCon Europe) is the premier conference for system architects, concept engineers, software developers, design and verification engineers, and IP integrators to share the latest methodologies and technologies on the practical use of EDA and IP languages and standards used in electronic design.

The focus of this highly technical conference is on the industrial application of specialized design, verification and validation methodologies, design and verification languages such as SystemC, SystemVerilog, Verilog, VHDL or e; assertions; the use of AMS languages; design automation using IP-XACT and the use of other general purpose languages.

This call for tutorials solicits high quality educational training sessions that are technical and reflect real life experiences in using EDA languages, standards, methodologies and tools. Industry applications of interest are (but not limited to) automotive, mobile communication, aerospace, healthcare, chip-cards, consumer and power electronics. Submissions are encouraged in (but not restricted to) the topic areas listed below:

- Electronic System Level (ESL) design including: architectural and algorithmic exploration; interface-based design; transaction level modeling (TLM), etc.
- Verification and post-silicon-validation methodologies, their management and the traceability of the whole flow.
- Mixed-signal design, verification and modelling.
- Using the Universal Verification Methodology (UVM) for functional and coverage-driven verification.
- Assertion-based Verification (in a digital and in an AMS context).
- Hardware/software co-design or co-verification, using simulation, acceleration or emulation.
- Low-power design techniques using standards like UPF, CPF, IEEE1801, etc.
- Design or verification for functional safety (e.g., ISO 26262, DO-254).

DVCon Europe tutorials are 90 minute sessions, which will be presented on **October 16, 2017**. Concerning the tutorial structure, there is the option to have a single speaker for the session, but it is also possible to have several speakers. The submitter is in both cases responsible to organize the tutorial and deliver the presentation material.

Please submit your 500-600 word tutorial abstract by **April 17, 2017**.

## TUTORIAL ABSTRACT SUBMISSION PROCESS

A tutorial abstract should contain:

- Abstract title, stating that this is a Tutorial submission
- Name, affiliation, phone number and email addresses for all speakers
- An introduction that specifies the context and motivation of the Tutorial submission
- A summary of the specific content of your Tutorial and intended audience
- Must be 500-600 words and maximum 3 pages
- There is no template for the tutorial abstract; please use the default Word template
- Provide enough details so that the Technical Program Committee can evaluate the potential quality and interest of your possible Tutorial at DVCon Europe
- Please submit your abstract via MPA CSB Navigation Center by **April 17, 2017**.

## TUTORIAL SELECTION

The Tutorial Chair, assisted by the Technical Program Committee, selects tutorials for inclusion in the DVCon Europe program. Tutorials are selected based on:

- Breadth of interest in the area and the timeliness of the topic
- Technical depth and breadth of the proposal
- Differentiation from other tutorials and special sessions
- Multiple viewpoints on the topic
- How well the topic fits within the overall content of the conference

DVCon Europe is dedicated to the success of the tutorial day! Once the selection is final, you will be sent detailed guidelines and deadlines to assist you with your planning.

## TUTORIAL ROLES

**Tutorial Organizer:** For the selected tutorial, the Organizer coordinates all tutorial activities with DVCon Europe, including ensuring that content is delivered in a timely manner and that the final presentation goes smoothly; follow-through is critical, the Organizer must interact with the Tutorial Chair.

- The Organizer writes the proposal for the tutorial and the abstract that is submitted for proposal evaluation.
- The Organizer selects and confirms the participation of the Presenter(s) (who could include the Organizer).
- The Organizer writes the material that will be included in the website and publications. It is very important that the Organizer write the material to help a potential attendee decide if they should attend this tutorial.
- The program material should describe the target audience and their expected level of familiarity with the topic (Expert/Intermediate/Beginner).

- An Organizer can propose multiple tutorials on aligned topics with different speakers.

Presenter: The Presenter is responsible for delivering the presentation.

## IMPORTANT DEADLINES

**April 17, 2017:** Tutorial abstract submission deadline.

**June 20, 2017:** Tutorial Accept/Reject Notification. Accepted tutorial organizers and presenters will be invited and agree to do the following:

- Submit the final version of the tutorial presentation
- Register for the conference
- Submit a copyright form

**October 16, 2017:** All accepted tutorial organizers and presenters agree to present their tutorial at the conference

Presenter requirements can be found in our Resource Center.

**Please note:** Consistent with the requirements for other DVCon Europe presentations, your presentation may contain your company logo only on the title slide.

## CONFERENCE SCHEDULE

**October 16, 2017** — Tutorials and exhibition

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## QUESTIONS?

Feel free to contact us for questions on the submission process at [support@mpassociates.com](mailto:support@mpassociates.com) or Jackie McIntosh [jackie@mpassociates.com](mailto:jackie@mpassociates.com)

DVCon Europe reserves the right to restructure all tutorial suggestions.



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## CALL FOR PANELS

**DVCon Europe** has become established as the premier design and verification conference in Europe, since its inception four years ago. The conference provides a platform for us as engineers to exchange and learn from technical experiences with other users, universities and EDA vendors. DVCon Europe 2017 will take place in Munich on October 16-17, and the Call-for-Panels is now open until **April 17, 2017**.

## PANEL PROPOSAL

DVCon is planning to host two highly focused panel discussions. DVCon is looking for panels that are lively, controversial, and provoke discussion on a specific topic of interest to the community. Panel sessions should not consist of paper presentations, but should have plenty of discussion engaging the audience. Panels are scheduled for 1 hour on **October 16-17**. DVCon will select which day the panels will be presented. Please make sure that moderator and panelists are available on the day before the panel for a final sync up on the panel.

DVCon will attempt to work with the original organizer in refining the panel, but if this is not successful, another organizer may be appointed. If multiple panel suggestions are submitted with similar topics, the committee may choose to accept one over the others, to merge the proposed panels, or to reject all of them.

## PROPOSAL SUGGESTION

Proposals should be 2-3 pages in length and should contain:

- The topic, if possible formulated as a provocative question
- The issues to be discussed, including a short listing of pro and con arguments
- Short biographies of the moderator and prospective panelists
- Any special requirements



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## PANEL SCHEDULE

- **April 17, 2017:** Panel proposals due
- **June 20, 2017:** Accept/reject notification
- **July 18, 2017:** All panel content due for Conference Program and website: panel title, abstract, panelist names, affiliations, and biographies
- **October 16-17, 2017:** DVCon Europe 2017 conference

Feel free to contact Joachim Geishauser at [Joachim.geishauser@nxp.com](mailto:Joachim.geishauser@nxp.com) or Jackie McIntosh at [jackie@mpassociates.com](mailto:jackie@mpassociates.com) for questions on the submission process.