



## **DVCon Europe Transforms to Virtual Event and Announces Keynotes from Industry Luminaries**

**Munich, Germany – 1 July, 2020** - The Design and Verification Conference & Exhibition Europe (DVCon Europe), sponsored by Accellera Systems Initiative, is to be transformed into a virtual event and will be held from October 27<sup>th</sup> to 28<sup>th</sup> 2020, with SystemC Evolution Day on the 29<sup>th</sup>. The first two keynote speakers, Victoria (Vicki) Mitchell, VP Systems Engineering at Arm, and Dr. Matthias Traub, Head of Architecture and Technologies at Volkswagen, are already on board.

Vicki Mitchell's keynote will be entitled 'The Benefits of Hardware DevOps', and Dr. Matthias Traub will present 'Hardware/Software Codesign for Automotive Electronics – A Paradigm Shift'.

Said Joachim Geishauser, General Chair of DVCon Europe, "We are very pleased to have Vicki and Matthias on board and both keynotes fit perfectly with our ambition to further strengthen the collaboration and interaction between software, hardware and system-level experts, and jointly develop the standards and methodologies required to design and verify the products of the future."

Vicki Mitchell joined Arm in 2018, bringing over 30 years' experience in low-level systems software, gained at semiconductor companies including Altera, Intel and Cirrus Logic.

Dr. Matthias Traub has extensive experience in automotive electronics, including 10 years with BMW AG. Before joining Volkswagen in December 2019, he was Head of E/E Architecture, Security, Connectivity and Computing Platforms at BMW.

The 2020 conference will include virtual papers, tutorials and panels where industry experts share best practices on system-level engineering and embedded software, advanced verification and validation, IP reuse and design automation, functional safety and security, and mixed-signal and low-power design and verification. Special interest areas are artificial intelligence, hardware/software co-design, RISC-V, next generation automotive and 5G.

## **ABOUT DVCON EUROPE**

The Design and Verification Conference & Exhibition in Europe (DVCon Europe) is the leading European event covering the application of languages, tools and intellectual property for the design and verification of electronic systems and integrated circuits. Sponsored by [Accellera Systems Initiative](#), and one of several DVCon events around the globe, DVCon Europe brings chip architects, design & verification engineers, and IP integrators the latest methodologies, techniques, applications and demonstrations for the practical use of EDA solutions for electronic design. For more details, visit [www.dvcon-europe.org](http://www.dvcon-europe.org). Follow #dvconeurope on Twitter.

## **ABOUT ACCELLERA SYSTEMS INITIATIVE**

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization, dedicated to create, support, promote and advance system-level design, modeling and verification standards for use by the worldwide electronics industry. The organization accelerates standards development and, as part of its ongoing partnership with the IEEE, its standards are contributed to the IEEE Standards Association for formal standardization and ongoing change control. For more information, please visit [accellera.org](http://accellera.org). For membership information, please email [membership@accellera.org](mailto:membership@accellera.org). Follow @accellera on Twitter or to comment, please use #accellera.

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