Berthold Hellenthal of Audi, Horst Symanzik of Bosch Sensortec to Deliver Keynote Addresses at DVCon™ Europe 2017

Registration now open for DVCon Europe 2017

Munich, Germany - August 10, 2017 - The Design and Verification Conference ([DVCon](#)) Europe 2017 has announced two keynote speakers for the conference in Munich, Germany on October 16th and 17th, 2017. Dr. Horst Symanzik, the Director of Integrated Circuit Engineering at Bosch Sensortec will be talking about new development solutions employed at the Bosch sensor division. Berthold Hellenthal, Head of the Progressive Semiconductor Program at Audi AG will consider the latest design and verification challenges in the automotive space.

"DVCon Europe has grown quickly to become established as the premier event in Europe for electronic industry stakeholders to receive a practical and informative update on innovative design and verification techniques, with a local relevance," noted Oliver Bell, DVCon Europe General Chair. "I am delighted that both Berthold and Horst have agreed to speak at the conference. Their valuable insights into the automotive sector and electronic design in general will complement the excellent conference program."

Dr. Symanzik will deliver a [keynote](#) entitled "Consumer MEMS Products: Quality rather than Commodity" on October 16th, the first day of the conference. Modern consumer sensor products are extremely complex, often containing integrated microcontrollers, digital signal processors, image pipelines, external components and more. At the same time, MEMS application complexity has grown significantly, from terrestrial magnetic, acceleration and yaw sensors to new systems such as the micro mirror. This keynote will discuss some of the challenges Bosch Sensortec is facing in its ASIC development for current and future MEMS products. Special attention is paid to the changes in the design and verification methodology, which have become necessary in order to tackle these challenges, originating from a mixed-signal heritage.

On October 17th Mr. Hellenthal will deliver the second conference [keynote](#) address, entitled "Driving Virtual Prototyping of Automotive Electronics." Automotive electronic systems represent the state of the art in modern electronic design. Advanced technology requirements that include Machine Learning, Internet of Things (IoT) and other leading edge areas are coupled with stringent methodology needs, such as safety critical verification. This talk will focus on this branch of semiconductor and systems design, exploring new techniques for the rapid prototyping of automotive solutions.

DVCON EUROPE 2017 PROGRAM AND REGISTRATION

Registration is now open for the 2017 DVCon Europe. The conference has grown rapidly since its inception four years ago. It now attracts several hundred electronics engineers and managers from all over Europe and beyond, including representatives from all the industry leading semiconductor and systems companies on the continent. Its focus is the practical application of the latest design and verification techniques employed on electronic semiconductors and systems.

The conference program offers a broad and extensive range of tutorials, papers, panels, and keynote speeches. The event also includes an exhibition, receptions and other opportunities for industry networking and interaction. Primary areas of interest covered include:

- System Level Design & Verification
- Advanced Verification & Validation
- Design for Functional Safety
- IP Reuse and Design Automation
- Analog and Mixed-Signal Design & Verification
- Lower Power Techniques

ABOUT THE SPEAKERS

Dr. Horst Symanzik has received his Diploma degree in Electrical Engineering (EE) from RWTH Aachen and holds a PhD degree in EE from TU Chemnitz where he was a research associate for MEMS electronic circuit design. He worked in semiconductor industry in several positions as a mixed-signal design engineer, ASIC project manager and wireless IC development manager. In 2007 he joined Bosch Sensortec, the new established MEMS Consumer Sensor branch of Bosch, to build up a non-automotive IC development organization. As the Director of Engineering Integrated Circuits, he has grown his organization in five international locations of the Bosch Consumer ASIC supplier, with more than 5 billion devices in the field.

Berthold Hellenthal runs the Progressive Semiconductor Program (PSCP) at the Audi Competence center for Electronics and Semiconductors, and is responsible for the company's semiconductor strategy. Having joined Audi in 2008 as a member of its senior management team, Mr. Hellenthal specializes in electronic hardware reviews and application analysis as well as semiconductor programs. Mr. Hellenthal holds a Dipl.-Ing from RWTH Aachen University in Germany.

ABOUT DVCON EUROPE

The Design and Verification Conference (DVCon) Europe is the leading European event covering the application of languages, tools and intellectual property for the design and verification of electronic systems and integrated circuits. Sponsored by Accellera Systems Initiative™, DVCon Europe brings chip architects, design and verification engineers, and IP integrators the latest methodologies, techniques, applications and demonstrations for the practical use of EDA solutions for electronic design. One of three DVCon events around the globe, DVCon Europe 2017 will include 2 keynote speeches, panel

sessions, a broad range of papers and posters, 16 tutorials and an exhibition with demonstrations from leading tool, IP and service providers. It will be held at the Holiday Inn Munich City Center, Munich, Germany on the 16th and 17th of October, 2017. For more details and registration, visit www.dvcon-europe.org. Follow DVCon Europe on #dvconeurope.

ABOUT ACCELLERA SYSTEMS INITIATIVE

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization, dedicated to create, support, promote and advance system-level design, modeling and verification standards for use by the worldwide electronics industry. The organization accelerates standards development and as part of its ongoing partnership with the IEEE, its standards are contributed to the IEEE Standards Association for formal standardization and ongoing change control. For more information, please visit accellera.org. For membership information, please email membership@accellera.org.

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