



DVCon Europe 2016 Gains in Popularity, Attendance Grows By 20% Over Previous Year

Conference Established as the Premier, Must-Attend Event for Electronic Design and Verification in Europe

Munich, Germany - November 15th, 2016 - The Design and Verification Conference & Exhibition Europe (DVCon Europe) 2016, sponsored by Accellera Systems Initiative, concluded with a 20% increase in attendance over the 2015 event. The conference also attracted a greater number of exhibitors as well as paper and tutorial submissions.

The overall registration number for DVCon Europe 2016 was 288 participants. The attendees represented more than 93 separate companies and organizations from 25 countries. The number of industry-oriented paper and tutorial submissions also increased significantly, and the event attracted 24 exhibitors. This year's conference included three keynote speeches from industry luminaries, two panel sessions and a gala dinner attended by 140 people.

The award for Best Paper, as voted by conference attendees, went to Rafal A. Baranowski & Marco Trunzer of Robert Bosch GmbH for their paper titled "Complete Formal Verification of a Family of Automotive DSP."

"It's fantastic to see this event continuing to do so well, meeting a clear need for a European forum that provides practical, detailed information on state-of-the-art development methodologies," noted Oliver Bell, DVCon Europe General Chair. "This year's conference was particularly exciting with three dynamic keynote speeches, overwhelming tutorial and paper submissions, and a vibrant exhibition. Now that DVCon Europe is established as the must-attend event in Europe for Engineers to upgrade their skills, we are looking forward to an even larger event in 2017."

HIGHLIGHTS OF THE WEEK

The first day of the conference commenced with an insightful keynote from Hobson Bullman, the General Manager of ARM's Technology Services Group, on the Design and Verification challenges faced by ARM and their customers. The day featured a series of 16 tutorials, providing attendees with a rich choice of technical updates and instruction on subjects including UVM-based Verification, SystemC System Level Verification, Safety-Critical Verification, Accellera's emerging Portable Stimulus Standard, Formal Verification and Analog/Mixed-Signal Design and others. The day was completed with a well-attended gala dinner, featuring a speech by Bob Smith, the Executive Director of the Electronic Systems Design Alliance.

The second day started with a keynote from Juergen Weyer, Vice President of Automotive Sales for EMEA at NXP Semiconductors, entitled, "The Road Ahead for the Securely Connected, Self-Driving Car." The day then featured 13 paper sessions covering the topical subjects, including UVM, Formal, Mixed Signal, SystemC, and Advanced Verification Techniques. New to this year's event were two, well-received "Lightning Talk" sessions, which featured shorter design oriented presentations. The day also included two panel sessions, "Security in the Automotive Value Chain" and an "Accellera Townhall," where standardization plans and processes were covered in an interactive session. Running coincidentally was the well-attended exhibit where the latest tools and IP from leading vendors was on show for all attendees.

The DVCon Europe Steering Committee values all feedback regarding the conference. Attendees have been given a survey and are asked to provide input on how to make DVCon Europe 2017 even better.

DVCON AROUND THE GLOBE IN 2017

DVCon Europe 2017 will be held at the Holiday Inn in Munich, Germany on October 16 and 17, 2017; details to be published soon. DVCon U.S. 2017 will be held February 27 - March 2, 2017 at the DoubleTree Hotel in San Jose, CA. DVCon China will take place at the Parkyard Hotel, Shanghai on April 19, 2017. DVCon India is planned for September 2017.

ABOUT DVCON EUROPE

The Design and Verification Conference & Exhibition in Europe (DVCon Europe) is the leading European event covering the application of languages, tools and intellectual property for the design and verification of electronic systems and integrated circuits. Sponsored by [Accellera Systems Initiative](#), and one of four DVCon events around the globe, DVCon Europe brings chip architects, design & verification engineers, and IP integrators the latest methodologies, techniques, applications and demonstrations for the practical use of EDA solutions for electronic design. For more details, visit www.dvcon-europe.org. Follow #dvconeurope on Twitter.

ABOUT ACCELLERA SYSTEMS INITIATIVE

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization, dedicated to create, support, promote and advance system-level design, modeling and verification standards for use by the worldwide electronics industry. The organization accelerates standards development and, as part of its ongoing partnership with the IEEE, its standards are contributed to the IEEE Standards Association for formal standardization and ongoing change control. For more information, please visit accellera.org. For membership information, please email membership@accellera.org. Follow @accellera on Twitter or to comment, please use #accellera.

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