



## **DVCon Europe 2018 Announces Call for Papers, Tutorials, Panels for Design and Verification of Complex Electronic Systems and Devices**

***Europe's premier electronic system development conference focuses on advanced verification, complex System-on-Chip solutions, analog/mixed-signal development, safety and security***

**Munich, Germany – March 15th, 2018** - The Design and Verification Conference & Exhibition Europe (DVCon Europe), sponsored by Accellera Systems Initiative, has announced the call for papers, tutorials and panels for the next conference to be held in Munich Germany on October 24th and 25th, 2018.

DVCon Europe is the premier technical conference in Europe focused on state-of-the-art design and verification techniques that target the most complex electronic systems and devices development challenges. It provides attendees with practical, informative detail on the latest design and verification methodologies, techniques and technologies shared by Electronic Design Automation (EDA) end-users and tool vendors alike.

The 2018 conference invites papers, tutorials and panels on a range of topics, including: Electronic System Level (ESL) design, verification and validation, Intellectual Property (IP) reuse and design automation, functional safety and security, and mixed signal & low power design & verification. Topics of particular interest include: architectural and algorithm exploration techniques including the use of SystemC, verification methodologies that involve the Universal Verification Methodology (UVM), mixed signal modeling and verification, formal and assertion-based verification, hardware/software co-design/co-verification, working with safety & security standards such as ISO 26262 and DO-254, and application of the emerging Accellera Portable Test and Stimulus Standard (PSS).

“2018 will be a year of exciting developments using established as well as emerging EDA standards, tools and methodologies,” noted Martin Barnasconi, DVCon Europe 2018 General Chair. “DVCon Europe has proven to be the place for technical experts to learn about these new techniques and their application in Europe. Since its inception five years ago, the conference has earned a reputation for technical excellence, where a published paper receives broad interest and acclaim. Don't miss this opportunity to be part of the growing DVCon Europe community.”

Initial submissions for papers should contain a minimum of 600 words and should provide enough technical detail for the Technical Program Committee to evaluate the proposed paper prior to writing a full manuscript. The deadline for paper submissions is April 11<sup>th</sup>, 2018. Tutorial submission abstracts of a maximum of 600 words or three pages are invited for the DVCon Europe 90 minute tutorial sessions. Panel proposals for two panel sessions are also invited of two pages in length. The deadline for tutorial

and panel submissions is April 18<sup>th</sup>, 2018. For more information on submissions including the submission portal, please visit [www.DVCon-europe.org](http://www.DVCon-europe.org).

### **DVCON AROUND THE GLOBE IN 2018**

DVCon Europe 2018 will be held at the Holiday Inn in Munich, Germany on October 24th and 25th, 2018. DVCon China will take place at the Doubletree by Hilton Shanghai-Pudong Hotel, Shanghai on April 18th, 2018. DVCon U.S. 2018 recently concluded and DVCon U.S. 2019 will be held February 25th-28th at the DoubleTree Hotel in San Jose, CA.

### **ABOUT DVCON EUROPE**

The Design and Verification Conference & Exhibition in Europe (DVCon Europe) is the leading European event covering the application of languages, tools and intellectual property for the design and verification of electronic systems and integrated circuits. Sponsored by [Accellera Systems Initiative](#), and one of several DVCon events around the globe, DVCon Europe brings chip architects, design & verification engineers, and IP integrators the latest methodologies, techniques, applications and demonstrations for the practical use of EDA solutions for electronic design. For more details, visit [www.dvcon-europe.org](http://www.dvcon-europe.org). Follow #dvconeurope on Twitter.

### **ABOUT ACCELLERA SYSTEMS INITIATIVE**

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization, dedicated to create, support, promote and advance system-level design, modeling and verification standards for use by the worldwide electronics industry. The organization accelerates standards development and, as part of its ongoing partnership with the IEEE, its standards are contributed to the IEEE Standards Association for formal standardization and ongoing change control. For more information, please visit [accellera.org](http://accellera.org). For membership information, please email [membership@accellera.org](mailto:membership@accellera.org). Follow @accellera on Twitter or to comment, please use #accellera.

ACCELLERA GLOBAL SPONSORS: Cadence; Mentor, A Siemens Business; Synopsys

###

Accellera Systems Initiative and DVCon are trademarks of Accellera Systems Initiative Inc. All other trademarks and trade names are the property of their respective owners.

Editorial Contact:

Annette Bley

Annette Bley PR

Phone: +44 (0) 7973 801132

Email: [annette@annettebleypr.com](mailto:annette@annettebleypr.com)