



DVCon Europe Announces Format and Dates for Eighth Edition and Issues Calls For Papers, Tutorials And Panels

Munich, Germany – 17th March, 2021 - The eighth Design and Verification Conference & Exhibition Europe (DVCon Europe), sponsored by Accellera Systems Initiative, will be held as a virtual event on 26th and 27th October, with SystemC Evolution Day on 28th. The calls for papers, tutorials and panels have been issued, with a deadline for initial submissions of 26th April.

The particular aim of this year’s event is to strengthen collaboration with the functional safety and security community in order to support the development of necessary standards and methodologies. Over the last two years, the scope of the event has been successfully broadened to include embedded software, and the on-going aim is to strengthen collaboration between software, hardware and system-level design and verification experts.

The new General Chair of DVCon Europe is Sumit Jha. Sumit has been involved with DVCon Europe since 2019, acting as Poster Chair in 2020. The DVCon Europe 2021 Steering Committee is composed of technical experts from NXP, Intel, Bosch Sensortec, ARM, Infineon, Ericsson and Qualcomm, ensuring that DVCon Europe is made by users for users.

“DVCon Europe has a special responsibility during these times as most of our peers are working from home offices with very little opportunity to share ideas. At the same time the demands and challenges we face are growing exponentially,” noted Jha. “Last year’s virtual conference was well received by attendees from 90 organisations and 28 countries, and we will be building on the success of our Virtual Experience Rooms, which allowed attendees to network as avatars in a 3D world.”

The 2021 conference invites [papers, tutorials and panels](#) focused on sharing best practices and industrial application of design and verification methodologies, languages and standards in areas such as: system-level and software design, model-based and model-supported software design, verification and validation, IP reuse and design automation, functional safety and security, and mixed-signal and low-power design and verification.

Submissions are encouraged to highlight industry themes and trends such as digital twin, machine learning, RISC-V, next generation automotive and 5G.

ABOUT DVCON EUROPE

The Design and Verification Conference & Exhibition in Europe (DVCon Europe) is the leading European event covering the application of languages, tools and intellectual property for the design and verification of electronic systems and integrated circuits. Sponsored by [Accellera Systems Initiative](#), and one of several DVCon events around the globe (see below), DVCon Europe brings chip architects, design & verification engineers, and IP integrators the latest methodologies, techniques, applications and demonstrations for the practical use of EDA solutions for electronic design. For more details, visit www.dvcon-europe.org. Follow #dvconeurope on Twitter.

ABOUT ACCELLERA SYSTEMS INITIATIVE

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization, dedicated to create,

support, promote and advance system-level design, modeling and verification standards for use by the worldwide electronics industry. The organization accelerates standards development and, as part of its ongoing partnership with the IEEE, its standards are contributed to the IEEE Standards Association for formal standardization and ongoing change control. For more information, please visit accelera.org. For membership information, please email membership@accelera.org. Follow @accelera on Twitter or to comment, please use #accelera.

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Editorial Contact:

Annette Bley

Annette Bley PR

Phone: +44 (0) 7973 801132

Email: annette@annettebleypr.com