



DVCon Europe 2022 Announces Format and Dates *Calls For Papers, Tutorials and Panels Issued*

Munich, Germany – 27th April, 2022 - The ninth Design and Verification Conference & Exhibition Europe (DVCon Europe), sponsored by Accellera Systems Initiative, will be held as a live event in Munich on 6th and 7th December, with SystemC Evolution Day on 8th. The calls for papers, tutorials and panels have been issued, with a deadline for initial submissions of 23rd May.

DVCon Europe 2022 will have a particular focus on system-level and verification solutions and the continuing need for co-operation between software and hardware engineering. This feeds into the on-going aim of the event; to build bridges between different communities and disciplines with cross-functional collaboration and knowledge exchange.

Sumit Jha, General Chair of the steering committee, said, “Our 2020 and 2021 virtual conferences were very well received by the community, with attendees from 90 organisations and 28 countries, and we are really looking forward to meeting in person once again”.

The DVCon Europe 2022 Steering Committee is composed of technical experts from NXP, Intel, Bosch Sensortec, ARM, Infineon, Ericsson and Qualcomm, ensuring that DVCon Europe is made by users for users.

Those submitting papers, tutorials and panels are encouraged to highlight industry themes and trends such as machine learning and AI, functional safety and security, digital twin, RISC-V, next generation automotive and 5G.

ABOUT DVCON EUROPE

The Design and Verification Conference & Exhibition in Europe (DVCon Europe) is the leading European event covering the application of languages, tools and intellectual property for the design and verification of electronic systems and integrated circuits. Sponsored by [Accellera Systems Initiative](#), and one of several DVCon events around the globe (see below), DVCon Europe brings chip architects, design & verification engineers, and IP integrators the latest methodologies, techniques, applications and demonstrations for the practical use of EDA solutions for electronic design. For more details, visit www.dvcon-europe.org. Follow #dvconeurope on Twitter.

Forthcoming worldwide DVCon events are:

DVCon China – May 25th, 2022

DVCon Japan – June 23rd, 2022

DVCon India – September 6th-7th, 2022

DVCon U.S. – February 27th – March 2nd, 2023

ABOUT ACCELLERA SYSTEMS INITIATIVE

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization, dedicated to create, support, promote and advance system-level design, modeling and verification standards for use by the worldwide electronics industry. The organization accelerates standards development and, as part of its ongoing partnership with the IEEE, its standards are contributed to the IEEE Standards Association for formal standardization and ongoing change control. For more information, please visit accellera.org. For membership information, please email membership@accellera.org. Follow @accellera on Twitter or to comment, please use #accellera.

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Editorial Contact:

Annette Bley

Annette Bley PR

Phone: +44 (0) 7973 801132

Email: annette@annettebleypr.com