

October 15–16, 2024 | Holiday Inn Munich – City Centre | Munich, Germany

## CALL FOR RESEARCH PAPERS

### DVCON EUROPE with Research Track for the second time

The Design and Verification Conference & Exhibition Europe (DVCon Europe) is the premier European technical conference on system, software, design, verification, validation and integration. It is a place where the latest methodologies and technologies of tools, languages, and standards for integrated and embedded systems and products are shared and discussed.

Applications of interest include (but not limited to) automotive, mobile communication, aerospace, healthcare, chip-cards, consumer and power electronics. DVCon Europe solicits submissions related to industrial application or research in design and verification. Special interest areas are Digital Twin, Internet-of-Things, Functional Safety and Security, ML/AI, ADAS and Digitalization.

DVCon Europe 2024 accepts submissions of industrial and academic papers, tutorials and panels with highly technical content reflecting real life experiences as well as research topics.

The following are example topics.

#### SYSTEM-LEVEL AND SOFTWARE DESIGN

- Virtual prototyping and platforms
- Transaction-level modelling (e.g., SystemC TLM)
- Hardware-assisted prototyping
- Machine Learning for Software design
- Platforms for AI acceleration
- High-level synthesis from ESL languages

#### VERIFICATION & VALIDATION

- Verification process, reuse and resource management
- Methods bridging between verification and validation
- Hardware/software co-verification
- Advanced methodologies, test benches, and flows (e.g., UVM, PSS)
- Formal and semi-formal V&V techniques

#### FUNCTIONAL SAFETY AND SECURITY

- Functional safety standard compliance (e.g., ISO 26262)
- Safety and security in verification and validation (e.g., ISO 21434)
- Requirements-driven design and verification including traceability
- New methods supporting functional safety, security and reliability

#### MODEL-BASED SYSTEMS ENGINEERING

- Modelling for Digital Twins and Digital Thread
- Software for development and test automation
- Model based methodologies, tools and techniques
- Modelling languages (e.g. SysMLv2)
- Interoperability of models and/or tools (e.g. FSS)
- Hardware/software/embedded co-design

#### IP REUSE & DESIGN AUTOMATION

- IP tagging, protection or security
- SoC and IP integration methods, flows, and tools
- Configuration management of IPs including different abstraction level
- Flow and tool automation (e.g., IP-XACT)

#### MIXED-SIGNAL AND LOW-POWER DESIGN AND VERIFICATION

- AMS modelling for concept and system-level design
- Application of mixed-signal extensions in verification (e.g., UVM-MS)
- Real-number modelling approaches
- Self-checking test benches in analog verification
- Low-power design and verification (e.g., UPF)

#### IMPORTANT DATES

**July 1, 2024: Full Paper Submission Deadline**

Academic TPC members: [dvcon-europe.org/technical-program-committee-research-track](https://dvcon-europe.org/technical-program-committee-research-track)

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**More information: visit [dvcon-europe.org](https://dvcon-europe.org)**

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## SUBMISSION GUIDELINES

- DVCon Europe runs a dedicated research track, submissions accepted into this track will be published as an academic paper.
- The program committee is looking for high quality research whether the authors are academics or come from an industrial background.
- Submissions to the Research track will be subject to a full peer review.
- Published as an academic paper through VDE Verlag in *IEEE Xplore*®

## SUBMISSION FORMAT

### Full Paper Submission

- The full paper must be submitted by July 1, 2024. It will be subject by blind review; the paper hence must not include the names of the authors in the title page.
- A submitter must enter the names, affiliations, cities, states, countries and email addresses of ALL the co-authors. The addition of new co-authors or re-ordering of authors will not be permitted after July 1.
- Title may not be changed after the full paper submission deadline of July 1.
- Authors will be asked to report Conflicts of Interest with TPC members.

## SUBMISSION RULES

- A submitter must enter the names, affiliations, cities, states, countries and email addresses of ALL the co-authors. The addition of new co-authors or re-ordering of authors will not be permitted after the initial abstract submission deadline.
- Title may not be changed after the abstract submission deadline.
- DO NOT identify the author(s) by their name(s) or affiliation(s) anywhere on the manuscript or abstract, with all references to the author(s)' own previous work or affiliations in the bibliographic citations being in the third person. Avoid the use of "omitted for blind review" in the bibliography section. Make sure the PDF metadata does not contain the author information.
- The abstract of approximately 100 words must clearly state the significant contribution, impact, and results of the manuscript.
- The manuscript must be within 6 pages, double-columned, 9 or 10-pt font, in PDF format only, be a readable file and follow the template. The length and content of the submitted version and that of the final version (if accepted) should not be significantly different.
- A submitter is required to check the conflicts of interests (COI) field and the duplicate submission field. See the "Conflict of Interest" section and the "Duplicate Submission" section for further information.

### An author of each accepted submission is required to:

- Produce a paper for the official conference proceedings.
- Sign and submit a copyright form.
- All accepted presentations are expected to submit a video of their presentation in advance of the event. This does not replace participation in the event.

### In addition:

- One co-author on the paper is required to pay the Speaker Registration Fee.
- The speaker must present the paper at the conference. If anyone other than a co-author of the paper presents the paper at the conference, the paper will be removed from the proceedings.

### Following DVCon policy on authorship, anyone listed as author on a paper must meet all the following criteria:

- They have made substantial intellectual contributions to some components of the original work described in the paper; and
- They have participated in drafting and/or revision of the paper; and
- They are aware that the paper has been submitted for publication; and
- They agree to be held accountable for any issues relating to correctness or integrity of the work

## IMPORTANT DEADLINE

### July 1: Deadline for Full Paper Submission