



SYSTEMS INITIATIVE



October 15-16, 2024 | Holiday Inn Munich – City Centre | Munich, Germany

CALL FOR PAPERS, TUTORIALS & PANELS

The Design and Verification Conference & Exhibition Europe (DVCon Europe) is the premier European technical conference on system, software, design, verification, validation and integration. It is a place where the latest methodologies and technologies of tools, languages, and standards for integrated and embedded systems and products are shared and discussed.

Applications of interest include (but not limited to) automotive, mobile communication, aerospace, healthcare, chip-cards, consumer and power electronics. DVCon Europe solicits submissions related to industrial application or research in design and verification. Special interest areas are Digital Twin, Internet-of-Things, Functional Safety and Security, ML/AI, ADAS and Digitalization.

DVCon Europe 2024 accepts submissions of industrial and academic papers, tutorials and panels with highly technical content reflecting real life experiences as well as research topics.

The following are example topics.

SYSTEM-LEVEL AND SOFTWARE DESIGN

- Virtual prototyping and platforms
- Transaction-level modelling (e.g., SystemC TLM)
- Hardware-assisted prototyping
- Machine Learning for Software design
- Platforms for AI acceleration
- High-level synthesis from ESL languages

VERIFICATION & VALIDATION

- Verification process, reuse and resource management
- Methods bridging between verification and validation
- Hardware/software co-verification
- Advanced methodologies, test benches, and flows (e.g., UVM, PSS)
- Formal and semi-formal V&V techniques

FUNCTIONAL SAFETY AND SECURITY

- Functional safety standard compliance (e.g., ISO 26262)
- Safety and security in verification and validation (e.g., ISO 21434)
- Requirements-driven design and verification including traceability
- New methods supporting functional safety, security and reliability

MODEL-BASED SYSTEMS ENGINEERING

- Modelling for Digital Twins and Digital Thread
- Software for development and test automation
- Model based methodologies, tools and techniques
- Modelling languages (e.g. SysMLv2)
- Interoperability of models and/or tools (e.g. FSS)
- Hardware/software/embedded co-design

IP REUSE & DESIGN AUTOMATION

- IP tagging, protection or security
- SoC and IP integration methods, flows, and tools
- Configuration management of IPs including different abstraction level
- Flow and tool automation (e.g., IP-XACT)

MIXED-SIGNAL AND LOW-POWER DESIGN AND VERIFICATION

- AMS modelling for concept and system-level design
- Application of mixed-signal extensions in verification (e.g., UVM-MS)
- Real-number modelling approaches
- Self-checking test benches in analog verification
- Low-power design and verification (e.g., UPF)

More information: visit [dvcon-europe.org](https://www.dvcon-europe.org)

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SUBMISSION GUIDELINES

DVCon Europe welcomes several forms of presentation:

ENGINEERING PAPERS

DVCon Europe particularly welcomes submissions and corresponding presentations that are highly technical and reflect real-life experiences and emerging trends in relevant domains.

Papers are submitted to a 2 phase review process:

The initial submission should be approximately 2 pages, not including diagrams, figures or tables. The final submission is expected to be between 6 and 8 pages inclusive of all material. Templates will be found on the submission site.

TUTORIALS

Tutorials should represent high quality educational and technical training sessions. Real life experiences in using EDA languages, standards, methodologies and tools for system and software design and verification shall be reflected.

A tutorial full paper should contain a maximum of 3 pages.

PANELS

DVCon Europe calls for panels that are lively, controversial, and provoke discussion on a specific topic of interest to the community, with plenty of discussion engaging also the audience.

Proposals should be 1-2 pages in length.

IMPORTANT DATES

April 22, 2024

Call for Engineering Papers

<https://dvcon-europe.org/call-for-engineering-papers>

Call for Tutorials

<https://dvcon-europe.org/call-for-tutorials>

Call for Panels

<https://dvcon-europe.org/call-for-panels>

Steering Committee members: dvcon-europe.org/steering-committee

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